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Title Interleaved mapping method and apparatus for accessi

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Abstract This invention provides a method for accessing memory generating a block index for a block of data, mapping the address of a memory based on the block index and a number of the memory, storing the block of data into the address, and repeating from the generating step, where each one of the block indexes map in turns to one physical different banks, and result in any logical adjacent block at different banks of the memory.

**Patent Right
Change**

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Inheritance Note	No
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